

1:0	RSET_sel	RSET功能选择 00: 电池内阻选择PIN 01: GPIO3 10: Reserved 11: Reserved	R/W	00
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1.10 GPIO_CTL0

Offset = 0x53 default 0x00

Bit(s)	Name	Description	R/W	Reset
7:5	-	-	-	-
4:0	GPIO_INEN	GPIO[4:0]input enable 0: Disable 1: Enable	RW	0

1.11 GPIO_CTL1

Offset = 0x54 default 0x00

Bit(s)	Name	Description	R/W	Reset
7:5	-	-	-	-
4:0	GPIO_OUTEN	GPIO[4:0]output enable 0: Disable 1: Enable 在开启 Output 之前, 需要先将 data 配好。	RW	0

1.12 GPIO_CTL2

Offset = 0x55 default 0x00

Bit(s)	Name	Description	R/W	Reset
7:5	-	-	-	-
4:0	GPIO_DAT	GPIO[4:0] DATA	RW	0

1.13 BATVADC_DAT0

Offset = 0xa2

Bit(s)	Name	Description	R/W	Reset
7:0	BATVADC[7: 0]	BATVADC 数据的低 8bit	R	X

1.14 BATVADC_DAT1

Offset = 0xa3

Bit(s)	Name	Description	R/W	Reset
7:6			-	-
5:0	BATVADC[13: 8]	BATVADC 数据的高 6bit VBAT=BATVADC*0.26855mv+2.6V	R	X

1.15 BATIADC_DAT0

Offset = 0xa4

Bit(s)	Name	Description	R/W	Reset
7:0	BATIADC[7: 0]	BATIADC 数据的低 8bit	R	X

1.16 BATIADC_DAT1

Offset = 0xa5

Bit(s)	Name	Description	R/W	Reset
7:6			-	-
5:0	BATIADC[13: 8]	BATIADC 数据的高 6bit VBAT=BATiADC*0.745985mA	R	X

1.17 BATOCV_DAT0

Offset = 0xa8

Bit(s)	Name	Description	R/W	Reset
7:0	BATOCV[7: 0]	BATOCV 数据的低 8bit	R	X