

1.6 CHG_DIG_CTL1

Offset = 0x26

Bit(s)	Name	Description	R/W	Reset
7		RESERVED		
6		电池类型内部寄存器设定还是外部 Vset PIN 设定选择 1: 外部 VSET PIN 设置 0: 内部寄存器设置 如果是该 bit 为 1, 可通过 0x24 寄存器的 bit6: 5 来设定 电池类型	RW	1
5: 0		RESERVED		

1.7 MFP_CTL0

Offset = 0x50 default 0x04 0

Bit(s)	Name	Description	R/W	Reset
7	L2_DRIVE	Drive capacity strengthen 0: disable 1: enable	R/W	0
6	L1_DRIVE	Drive capacity strengthen 0: disable 1: enable	R/W	0
5			R/W	0
4	VSET_DRIVE	Drive capacity strengthen 0: disable 1: enable	R/W	0
3	RSET_DRIVE	Drive capacity strengthen 0: disable 1: enable	R/W	0
2	LIGHT_DRIVE	Drive capacity strengthen 0: disable 1: enable	R/W	1
1	L4_DRIVE	Drive capacity strengthen 0: disable 1: enable	R/W	0

0	L3_DRIVE	Drive capacity strengthen 0: disable 1: enable	R/W	0
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1.8 MFP_CTL1

Offset = 0x51 default 0x00

Bit(s)	Name	Description	R/W	Reset
7:6	-		-	-
5:4	LIGHT_sel	LIGHT 功能选择 00: WLED 01: GPIO2 10: VREF 11: Reserved	R/W	00
3:2	L4_sel	L4 的功能选择 00: L4 01: GPIO1 10: Reserved 11: Reserved	R/W	00
1: 0	L3_sel	L3的功能选择 00: L3 01: GPIO0 10: IREF 11: Reserved	R/W	00

1.9 MFP_CTL2

Offset = 0x52 default 0x00

Bit(s)	Name	Description	R/W	Reset
7: 6	-	-	-	-
5:4			R/W	00
3:2	VSET_sel	VSET功能选择 00: 电池电压选择PIN 01: GPIO4 10: Reserved 11: Reserved	R/W	00